

<p>Form PTO-1449</p> <p style="text-align: center;"><b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b></p> <p style="text-align: center;">(Use several sheets if necessary)</p>	<p>Docket Number (Optional) <b>A5GN2420US</b></p>	<p>Application Number <b>101665827</b></p>
<p>Applicant <b>Patterson, Gregory Wayne</b></p>		
<p>Filing Date</p>		<p>Group Art Unit</p>

**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
fcl	5 2 3 1 3 9 9	7/27/93	Gorman, et al.	341	159	
	5 5 8 9 8 3 1	12/31/96	Knee	341	159	
	5 7 0 6 0 0 8	1/6/98	Huntley, et al.	341	156	
	5 9 9 0 8 1 4	11/23/99	Croman, et al.	341	118	
	6 0 1 4 0 9 7	1/11/00	Brandt	341	156	
	6 4 5 2 5 1 9	9/17/02	Swanson	341	120	

**FOREIGN PATENT DOCUMENTS**

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
					YES   NO

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

fcl	Petschacher, Reinhard, et al, "A 10b, 75MSPS Subranging A/D Converter with Integrated Sample and Hold", IEEE Journal of Solid-State Physics, Vol. 25, No. 6, December, 1990
fcl	Waltari, Mikko, et al, "reference Voltage Drive for Low Voltage CMOS A/D Converters", IEEE International Conference on Electronics, Circuits and Systems, Vol. 1, No. 6, 2001

<p>EXAMINER</p> <p style="text-align: center;"><i>[Signature]</i></p>	<p>DATE CONSIDERED</p> <p style="text-align: center;">07/27/2004</p>
---	--

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP§609; Draw line through